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Application Number:	10589595			
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Title of Invention:	Chip design verification apparatus and method			
First Named Inventor/Applicant Name:	Hyun-Ju Park			
Customer Number:	23413			
Filer:	Noah J. Hayward			
Filer Authorized By:				
Attorney Docket Number:	SUN-0166			
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	Specification		2	3	
	Claims		4	25	
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	Applicant Arguments/Remarks Made in an Amendment		27	36	
Warnings:					
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,	2 Specification	SubstituteSpecClean.pdf	259105	no	49
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3 Specification	SubstituteSpecMarked.pdf	274990	no	50	
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4 Fee Worksheet (PTO-06)	fee-info.pdf	29986	no	2	
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.